RESS MAIL LABEL NO.: EV 566 185 017 US

Form PTO-1449 Applicant:

Alireza Dabagh, et al.

Serial No.: Filing Date: 10/020,286

Att'y Docket No.: 13768.213

December 11, 2001

Group: 2185

Sheet 1 of 3

Confirmation No.:

8376

For:

ARCHITECTURE AND RUN-TIME ENVIRONMENT FOR

**NETWORK FILTER DRIVERS** 

## INFORMATION DISCLOSURE CITATIONS MADE BY APPLICANT

## U.S. Patent Documents

Examiner <u>Initial*</u>	Patent Number	Issue <u>Date</u>	Name	Class	Sub Class	Filing <u>Date</u>
gpca1.	6,130,868	10/10/00	Yamamoto	369	54	10/08/97
U.S. Patent Publications						
Examiner <u>Initial*</u>	Publication Number	Pub <u>Date</u>	Name	Class	Sub <u>Class</u>	Filing <u>Date</u>
<u> </u>	2001/0023449	09/20/01	Clark, et al.	709	231	12/15/00
Foreign Patent Documents						
Examiner <u>Initial</u> *	Document Number	Publ. <u>Date</u>	Country or Patent Office	Sub <u>Class</u>	Class	Trans- lation
<u> 10</u> ← A3.	CA 2325652	05/13/2001	Canada	H04L	12/24	

Date Considered:

Examiner:

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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#### Other Documents

# Examiner Initial\*

Wolthusen, Stephen D., "Security Policy Enforcement at the File System Level in the Windows NT Operating System Family" Computer Security Applications Conference, 2001, Proceedings 17<sup>th</sup> Annual Dec. 10-14, 2001, Pages 55-63.

A5. Hutchinson, Norman C., "RPC in the x-Kernel: Evaluating New Design Techniques" Operating Systems Review, ACM Headquarters, New York, Vol. 23, No. 5, January 1989, pages 91-101.

"Augmentation of the Microsoft TCP/IP Stack Via an Network Driver Interface Specification Intermediate Driver" IBM Technical Disclosure Bulletin, Vol. 41, No. 1, January 1998, pages 301-302.

A7. Russinovich, Mark, et al., "Examining the Windows NT Filesystem" Dr. Dobb's Journal, M&T Publ. February 1997, pages 42, 44-46, 48.

Risso Fulvio, et al., "An Architecture for High Performance Network Analysis" Computers and Communications, 2001 Proceedings, Sixth IEEE Symposium on July 3-5, 2001, pages 686-693.

In your change

5/21/06

Examiner:

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### References Cited by Applicants

While the filing of Information Disclosure Statements is voluntary, the procedure is governed by the guidelines of Section 609 of the Manual of Patent Examining Procedure and 37 C.F.R. §§ 1.97 and 1.98. To be considered a proper Information Disclosure Statement, Form PTO-1449 shall be accompanied by a copy of each listed patent or publication or other item of information and a translation of the pertinent portions of foreign documents (if an existing translation is readily available to the applicant), an explanation of relevance of each reference not in the English language, and should be submitted in a timely manner as set out in MPEP Sec. 609.

Examiners will consider all citations submitted in conformance with 37 C.F.R. § 1.98 and MPEP Sec. 609 and place their initials adjacent the citations in the spaces provided on this form. Examiners will also initial citations not in conformance with the guidelines which may have been considered. A reference may be considered by the Examiner for any reason whether or not the citation is in full conformance with the guidelines. A line will be drawn through a citation if it is not in conformance with the guidelines AND has not been considered. A copy of the submitted form, as reviewed by the Examiner, will be returned to the applicant with the next communication. The original of the form will be entered into the application file.

Each citation initialed by the Examiner will be printed on the issued patent in the same manner as references cited by the Examiner on Form PTO-892.

The reference designations "A1," "A2," etc. (referring to Applicant's reference 1, Applicant's reference 2, etc.) will be used by the Examiner in the same manner as Examiner's reference designations "A," "B," "C," etc. on Office Action Form PTO-1142.

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Examiner: Date Considered:

Jany Chury

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